



Substitute for form 1449A INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	09/843,486
Sheet 1 of 1				Filing Date	April 25, 2001
				First Named Inventor	Arash Hassibi
				Art Unit	2811
				Examiner Name	Quang D. Vu
				Attorney Docket Number	4363P004

OTHER ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
qv		MARIA DEL MAR HERSHENSON, CMOS Analog Circuit Design Via Geometric Programming, Dissertation submitted to the Department Of Electrical Engineering and The Committee On Graduate Studies, Stanford University, Stanford, California, November 1999, Chapter 8, pages 183 - 188. Relevant Section 8.2.4, first made available at the Stanford University Library on or about November 17, 2000.	

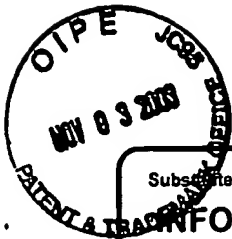
Examiner Signature		Date Considered	04/22/05
-------------------------------	--	----------------------------	----------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. ‡Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (05-03) as modified by Blakey, Solokoff, Taylor & Zafman (wlr) 05/02/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

RECEIVED
JUL 22 2003
TECHNOLOGY CENTER 2800



Substitute for form 1449A/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	09/843,486		
		Filing Date	April 25, 2001		
		First Named Inventor	Arash Hassibi		
		Art Unit	2811		
		Examiner Name	Quang D. Vu		
Sheet	1	of	1	Attorney Docket Number	4363P004

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
qv		HERSHENSON M DELM ET AL: "Optimal Design Of A CMOS OP-AMP Via Geometric Programming", IEEE Transactions On Computer-Aided Design Of Integrated Circuits and Systems, January 2001, IEEE Vol. 20, No. 1, Pages 1-21, Paragraph '007A', Table 1.	
qv		BALASA, F.; "Modeling Non-Slicing Floorplans With Binary Trees", 2000 IEEE/ACM International Conference On Computer-Aided Design. ICCAD-2000. San Jose, CA, Nov. 5-9, 2000, IEEE/ACM International Conference On Computer-Aided Design, New York, NY, IEEE, Nov. 5, 2000, Pages 13-16.	

Examiner Signature		Date Considered	02/22/05
--------------------	--	-----------------	----------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. ‡Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (08-03) as modified by Blakely, Sobkoff, Taylor & Zafman (wtr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Substitute for Form 1449/PTO

Complete if Known

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 4 Attorney Docket Number 004363.P004

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
qv		US-	6,578,179 B2	06-10-2003	Shirotori, et al.	
qv		US-	6,574,786 B1	06-03-2003	Pohlenz, et al.	
qv		US-	6,539,533 B1	03-25-2003	Brown, III et al.	
qv		US-	6,581,188	06-17-2003	Hosomi, et al.	
qv		US-	6,311,315	10-30-2001	Tamaki	
qv		US-	6,002,860	12-14-1999	Voinigescu, et al.	
qv		US-	5,754,826	05-19-1998	Gamal, et al.	
qv		US-	5,633,807	05-27-1997	Fishburn, et al.	
qv		US-	5,055,716	10-08-1991	El Gamel	
qv		US-	5,289,021	02-22-1994	El Gamel	
qv		US-	4,827,428	05-02-1989	Dunlop, et al.	
		US-				
		US-				
		US-				
		US-				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
qv		WO	01/37429 A1	05-25-2001	HORAN, et al.		

Examiner Signature		Date Considered	02/22/05
--------------------	---	-----------------	----------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.